

Heavy Ion SEE

Test Org.*	Device	Function	Technology	Mfr.	Effective SEU LET* Threshold	Device Xsection (cm ²)	Bits Tested	Bit Xsection (cm ²)	Test Date	LUth	LU Xsection (cm ²)	Fac.	Remarks	30-Aug-99
ADC (12-bit)														
Note: Entries in RED indicate data added since the 1997 Compendium.														
JPL	AD12062		CMOS/epi	ADI					Mar-97	12	3.0E-04	BNL	Unsat. @ LET = 60. Cross section plot exists.	
GSFC	AD1671JD	Internal reference, parallel out	Bipolar/CMOS	ADI					Apr-93	>90			LaBel, et al, 94IEEE Wrkshp Rec., pg 64. D/C 9219.	
NSWC/ ARSP	AD42961	Hard AD871	RBCMOS = RH BiCMOS	ADI	3	4.8E-03			1994	>126			Turflinger, 94IEEE TNS, No. 6, pg 2187. Noise cross section also exists.	
NSWC/ ARSP	AD42961	Flash. (hard AD871)	RBCMOS = RH BiCMOS	ADI	~10	1.0E-05			Apr-96	>126		UCB.	Turflinger, 94IEEE TNS, No. 6, pg 2187. Offset errors. Division by samples per irradiation recommended.	
NSWC/ ARSP	AD42961	Flash. (hard AD871)	RBCMOS = RH BiCMOS	ADI		5.0E-05			Apr-96	>126		UCB.	Turflinger, 94IEEE TNS, No. 6, pg 2187. Noise errors. Division by samples per irradiation recommended.	
JPL	AD574A	Internal reference	BiMOS	ADI	<3				Sep-91	>120				
JPL	AD574A	Internal reference	BiMOS	ADI	<3	1.5E-04			Sep-91	>110		UCB	8 MSBs tested.	
JPL	AD674A	Internal reference	Bipolar hybrid	ADI	<3	---			Sep-91	>120				
JPL	AD674B	Internal reference, parallel out	BIMOS II	ADI	<3	5.0E-04			Nov-92	>120				
JH	AD7672	Parallel and serial output	CMOS	ADI					Jan-91	>75			Kinnison	
JPL	AD7672B	Parallel and serial output	CMOS	ADI	6				Sep-91	>175			Kinnison	
JPL	AD7870	Internal reference	LC ² MOS non-hard	ADI					1995	>120			Nichols, et. al., 95IEEE Wrkshp Rec., pg 1. Tested up to 60 °C.	
SNL	AD7876	Parallel; two 8-bit bytes; or serial	LC ² MOS non-hard	ADI	2	2.0E-03			1994	>150			Sexton, 94IEEE Wrkshp Rec., pg 55.	
ESA	AD7893SQ	Serial 5.5 μs conv. time	LC²MOS	ADI	14	-2.5E-5			1997	>68		CYC	Bee, et al, 98IEEE Wrkshp Rec., pg 58.	
JPL	ADC574A	Internal reference	CMOS/bipolar	BUB	<40				Jul-91	<<40				
JPL	ADC674	Internal reference	CMOS/bipolar	BUB	<40				Jul-91	<<40				
TRW	ADC85	Int. ref. Parallel & serial out		SIP					Jul-92	>60			T.C. Lunn	
TRW	ADC87	Int. ref. Parallel & serial out		BUB					Jul-92		3.0E-05		T.C. Lunn. LU _{th} = ?. Cross section @ LET = 60.	
LIN	ADS112	Internal reference, parallel out		DAT					Apr-91	38				
GSFC	CS5012A		CMOS	CRY	3.5 to 4.8				Mar-97	11			LaBel, EEE Links, Vol. 3, No. 1, Mar 97	
JPL/HON	HI574	Internal reference	CMOS	HAR	10	8.0E-05			Dec-94	>120			D/C 9210	
JPL	HI674ALD	Internal reference	DC9205	HAR	6	1.0E-04			Nov-92	>120			Tested to 80 °C. Earlier D/C (next entry) latches up.	
JPL	HI674ASD	Internal reference	DC9028	HAR	6				Nov-92	30			See preceding entry.	
JPL	HI774B	Internal reference, parallel out	BICMOS	HAR					Nov-92	<<60			D/C 9022. Also has latchups with ²⁵² Cf.	
GSFC	HS5212	Int. ref. Parallel & serial out	Hybrid	SIP	~1.4	1.0E-03			Feb-94	>80			LaBel, 1994 SEE Symposium.	

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